

**ABSTRACT OF THE DISCLOSURE**

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2       An active matrix display device having a display region  
3 consisting of sub-pixels arrayed in a matrix fashion, the  
4 sub-pixels having switching elements, comprising a plurality  
5 of data and scan signal lines, and common voltage lines for  
6 sending signals and a reference voltage to the sub-pixels,  
7 test transistors, each of which is connected to one of the  
8 plurality of scan signal lines for sending test signals  
9 thereto, and a plurality of input terminals, each of which  
10 is connected to one of a plurality of the test transistors,  
11 wherein each gate of the test transistors and each of the  
12 common voltage lines are connected to one of the input  
13 terminals, the test transistors control inputs of the test  
14 signals to the sub-pixels.